

60 Storage Device Input Device Microprocessor Outputk 66 Encryption Processor 70

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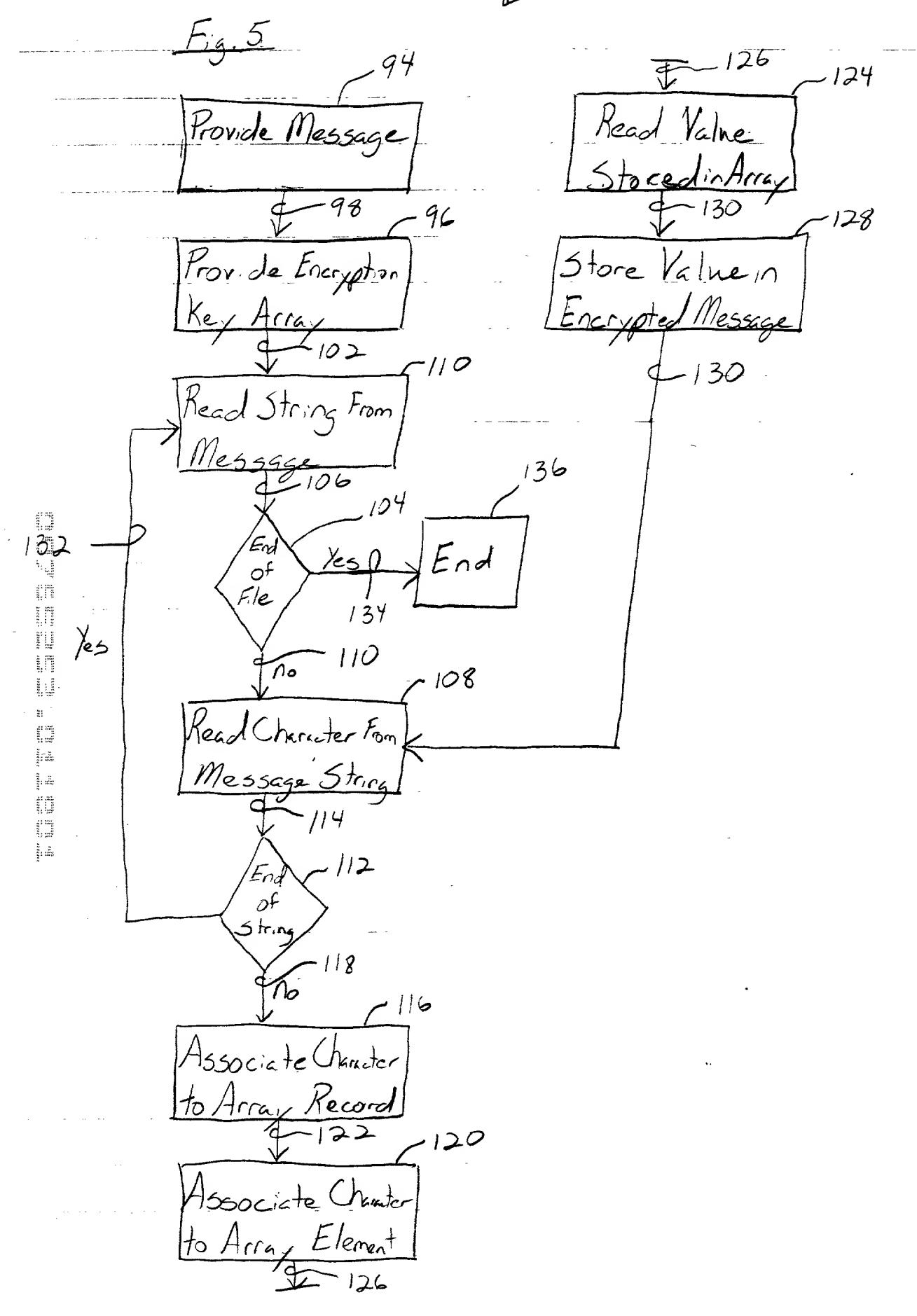
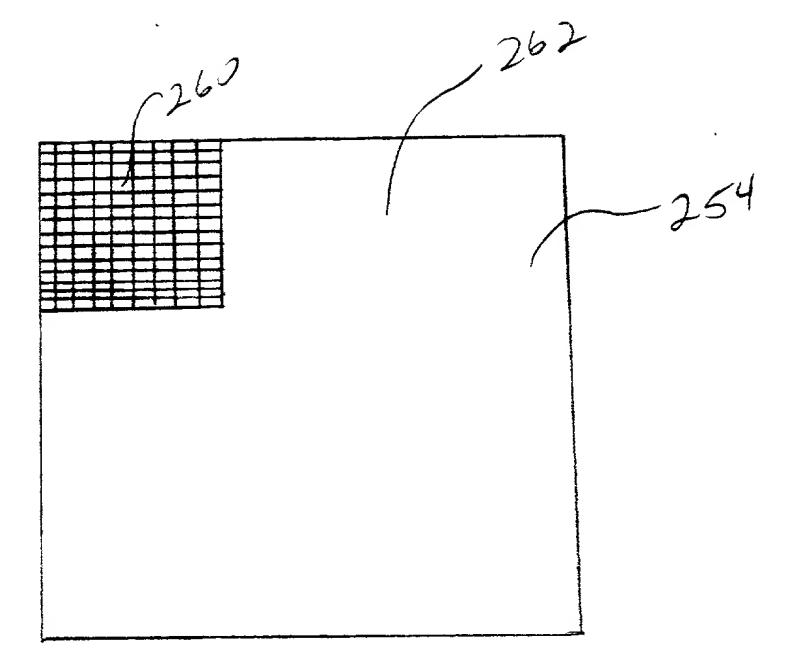


Fig. 8



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